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Approved for use through 10/31/2002, OMB 0651-0031

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Substituted for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Complete if Known	
Application Number			
Filing Date	09 July 2003 (herewith)		
First Named Inventor	William K. Bischel et al.		
Group Art Unit			
Examiner Name			
Attorney Docket Number	GEMF 2048-1		

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
GRK ↓ ↓	A21	6,343,171		Yoshimura et al.	01-29-2002	
	A22	6,445,841		Gloeckner et al.	09-03-2002	
	A23	6,456,766		Shaw et al.	09-24-2002	
	A24	6,483,969		Yap et al.	11-19-2002	
	A25					
	A26					
	A27					
	A28					
	A29					
	A30					
	A31					
	A32					
	A33					
	A34					
	A35					
	A36					
	A37					
	A38					
	A39					
	A40					

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
	B11							
	B12							
	B13							
	B14							
	B15							
	B16							
	B17							
	B18							
	B19							
	B20							

Examiner Signature	Date Considered 2/15/04
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Sheet	1	of	Application Number	
			Filing Date	09 July 2003 (herewith)
			First Named Inventor	William K. Bischel et al.
			Group Art Unit	
			Examiner Name	
			Attorney Docket Number	GEMF 2048-1

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		Number	Kind Code ² (if known)			
GRK	A1	5,010,587		Eklund	04-23-1991	
	A2	5,179,609		Blonder et al.	01-12-0993	
	A3	5,416,871		Takahashi et al.	05-16-1995	
	A4	5,544,268		Bischel et al.	08-06-1996	
	A5	5,621,837		Yamada et al.	04-15-1997	
	A6	5,985,064		Sato	11-16-1999	
	A7	5,985,685		Lealman et al.	11-16-1999	
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	A9	6,021,149		Miyazaki et al.	02-01-2000	
	A10	6,049,641		Deacon et al.	04-11-2000	
	A11	6,081,369		Waarts et al.	06-27-2000	
	A12	6,081,638		Zhou	06-27-2000	
	A13	6,088,492		Kaneko et al.	07-11-2000	
	A14	6,129,804		Gaynes et al.	10-10-2000	
	A15	6,169,757		Merritt	01-02-2001	
	A16	6,235,141		Feldman et al.	05-22-2001	
	A17	6,236,773		Butler et al.	05-22-2001	
	A18	6,243,350		Knight et al.	06-05-2001	
	A19	6,298,027		Wilde et al.	10-02-2001	
V	A20	6,316,281		Lee et al.	11-13-2001	

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		Office ³	Number ⁴	Kind Code ⁵ (If known)				
GRK	B1		WO 02/065600		Gemfire Corp.	08-22-2002		
	B2							
	B3							
	B4							
	B5							
	B6							
	B7							
	B8							
	B9							
	B10							

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Substitute for form 1449B/PTO		Complete if Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number		
		Filing Date	09 July 2003 (herewith)	
		First Named Inventor	William K. Bischel et al.	
		Group Art Unit		
		Examiner Name		
Sheet		of	Attorney Docket Number	GEMF 2048-1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
GRK	C1	ARMIENTO, C.A. et al., "Passive coupling of InGaAsP/InP laser array and singlemode fibres using silicon waferboard," Electronics Letters 27(12) (06/10/1991) 1109-1110, IEEE.	
GRK	C2	JACKSON, K.P., et al., "A high-density, four-channel, OEIC transceiver module utilizing planar-processed optical waveguides and flip-chip, solder-bump technology," J. Lightwave Tech. 12(7) (July 1994) 1185-1191, IEEE.	
GRK	C3	TAN, QING et al., "Thermosonic Flip-Chip Bonding System with a Self-Planarization Feature Using Polymer," Proceedings of the 48th Electronic Components and Technology Conference (ECTC 1998), 1318-1325, article can be found at	
GRK	C4	WALE, MICHAEL J., et al., "Self-Aligned Flip-Chip Assembly of Photonic Devices with Electrical and Optical Connections," IEEE Transactions on Components, Hybrids and Manufacturing Technology 13(4) (Dec. 1990) 780-786, IEEE.	
GRK	C5	Copy of co-pending application Serial No. 10/336,293 filed 03-Jan-2003 entitled "Packaging and Alignment Methods for Optical Components, and Optical Apparatus Employing Same," which is a continuation of Application No. 09/784,945 filed 14 February 2001.	

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